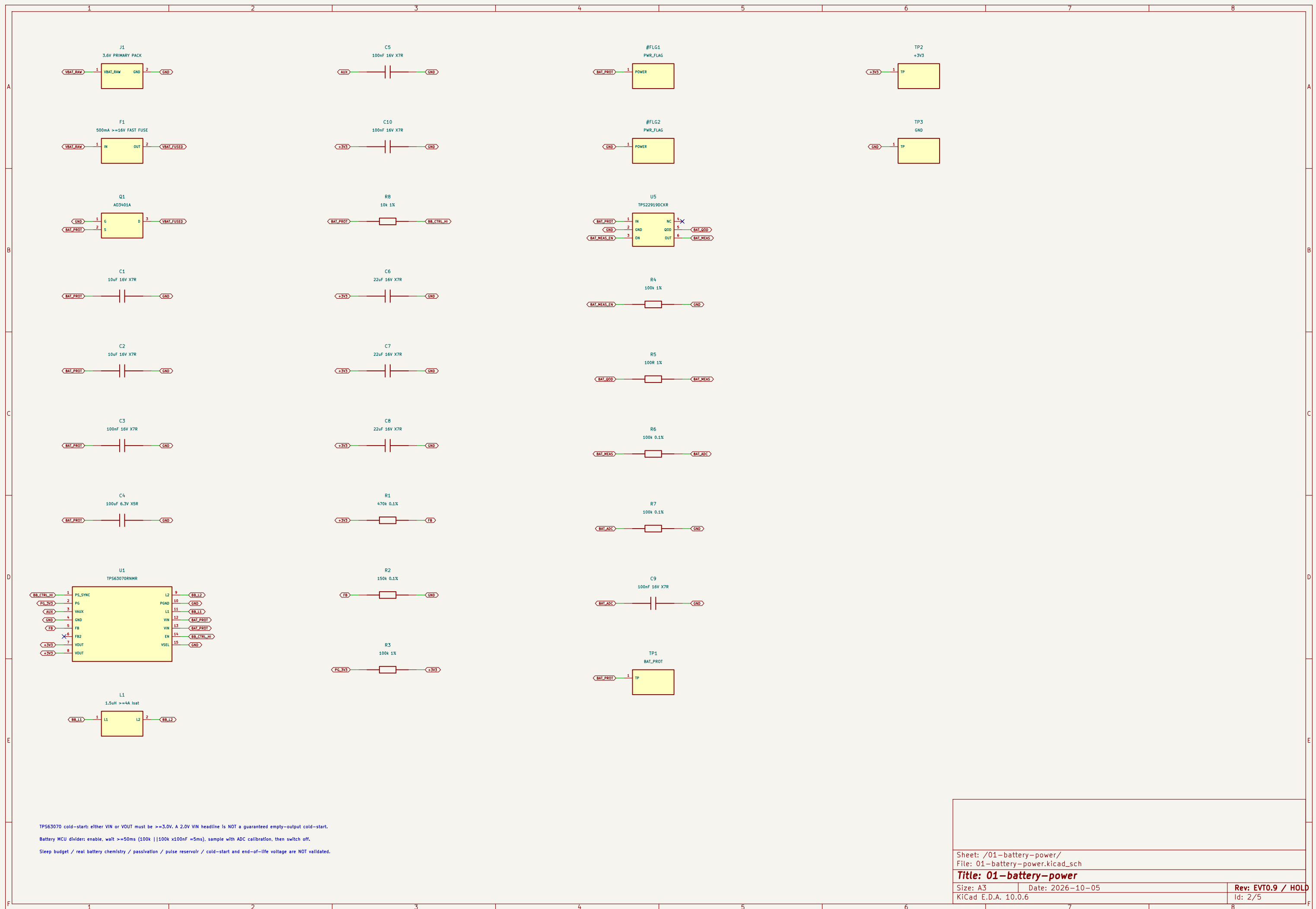
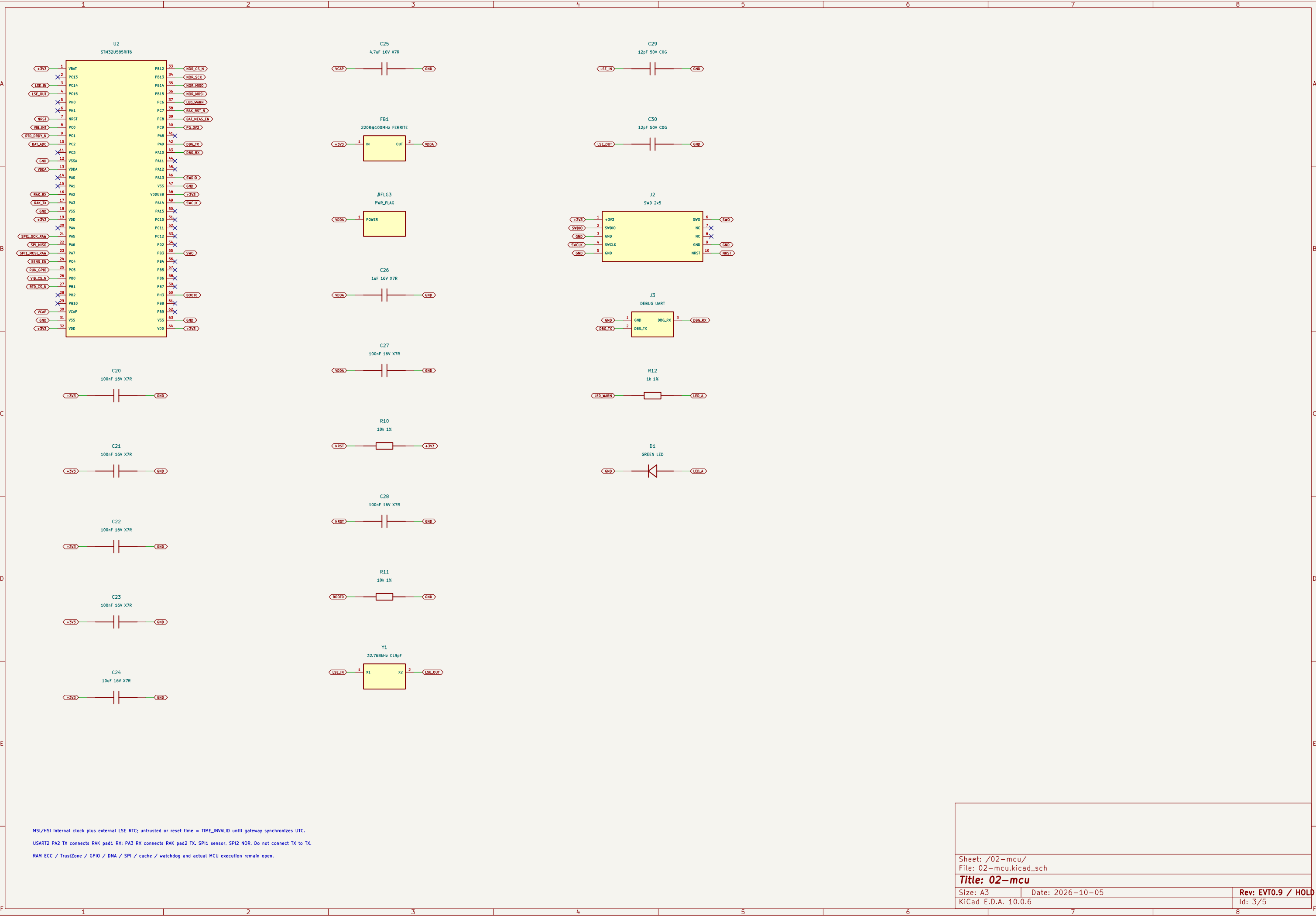
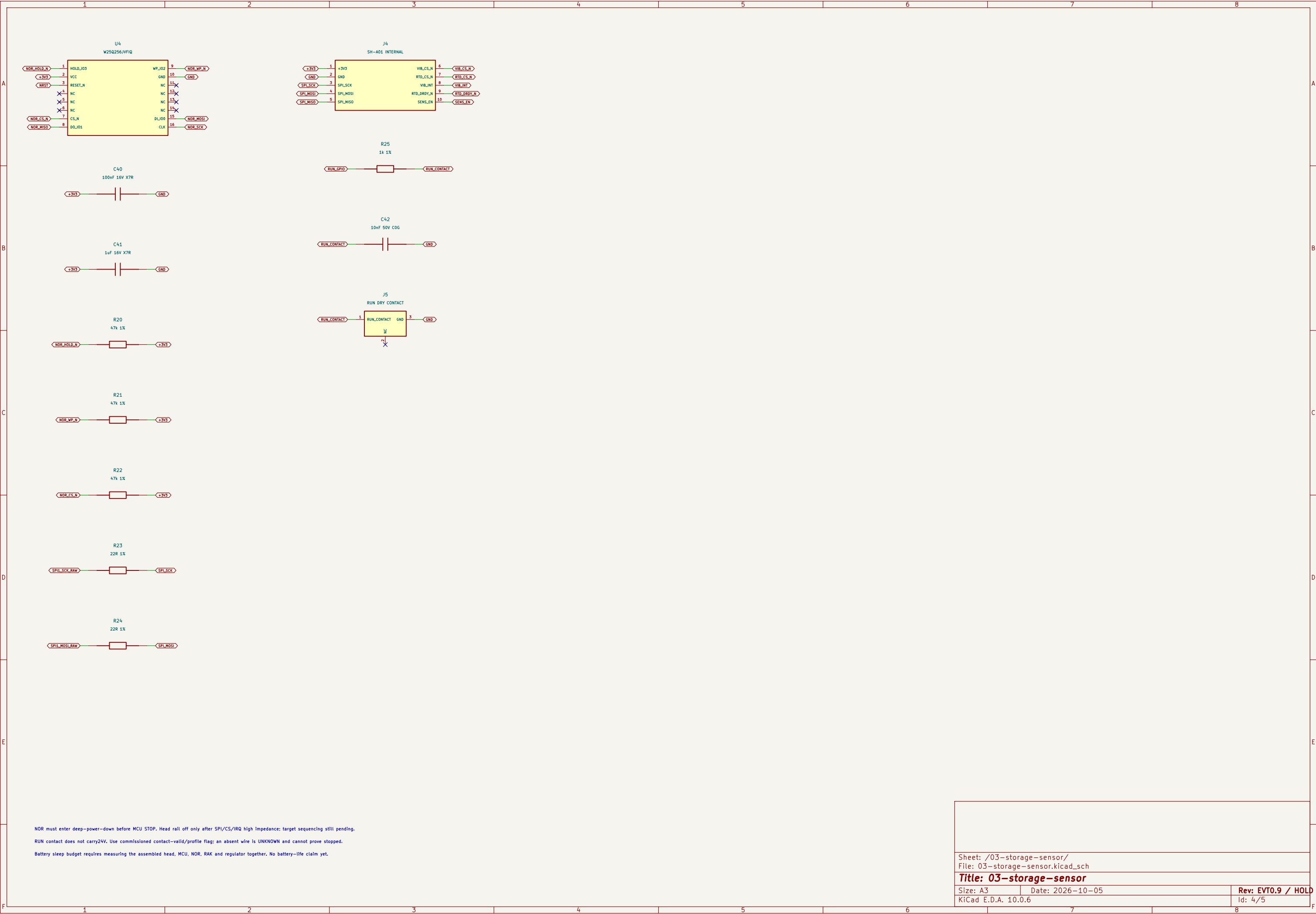


	1	2	3	4	5	6	7	8
A	01–battery–power 27 placed–part definitions; labels shared globally File: 01–battery–power.kicad_sch				02–mcu 20 placed–part definitions; labels shared globally File: 02–mcu.kicad_sch			
B								
C	03–storage–sensor 12 placed–part definitions; labels shared globally File: 03–storage–sensor.kicad_sch				04–lora 6 placed–part definitions; labels shared globally File: 04–lora.kicad_sch			
D								
E								
F	ENGINEERING CANDIDATE – NO FABRICATION RELEASE. Power/RF/DFM review, BOM freeze and physical EVT remain open.				Sheet: / File: MB–L01.kicad_sch Title: MB–L01 – anomaly monitor Size: A3Date:KiCad E.D.A. 10.0.6 Rev: EVT0.9 / HOLDId: 1/5			
	1	2	3	4	5	6	7	8







NOR must enter deep-power-down before MCU STOP. Head rail off only after SPI/CS/IRQ high impedance; target sequencing still pending.

RUN contact does not carry24V. Use commissioned contact-valid/profile flag; an absent wire is UNKNOWN and cannot prove stopped.

Battery sleep budget requires measuring the assembled head, MCU, NOR, RAK and regulator together. No battery-life claim yet.

